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PATENT & TRADEMARK OFFICE

EDWARD J. NOWAK
BUR920030159US1 (WDS)
SHEETS 1 OF 10

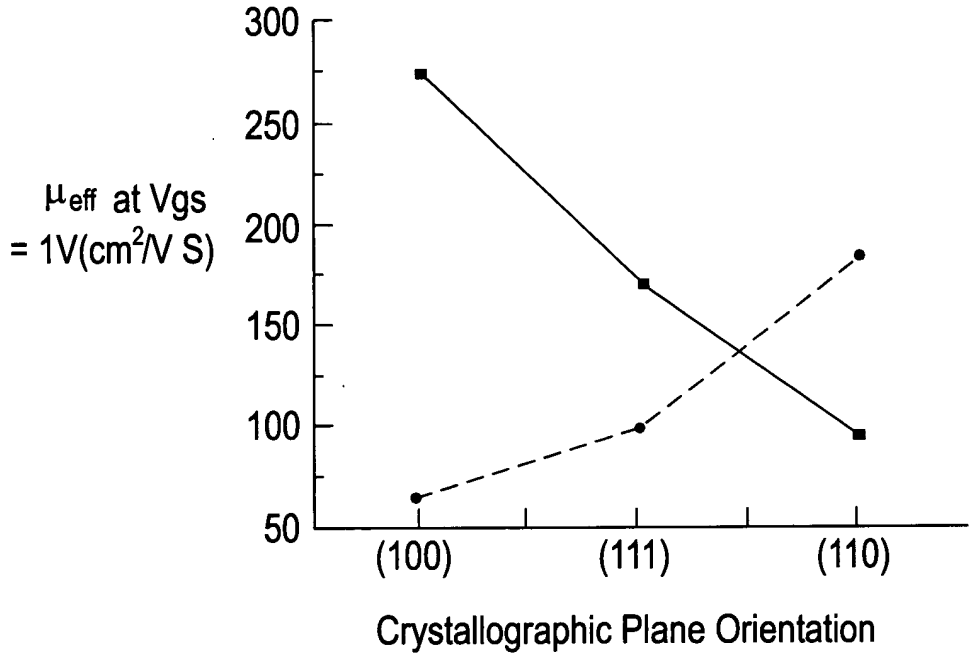

Fig. 1

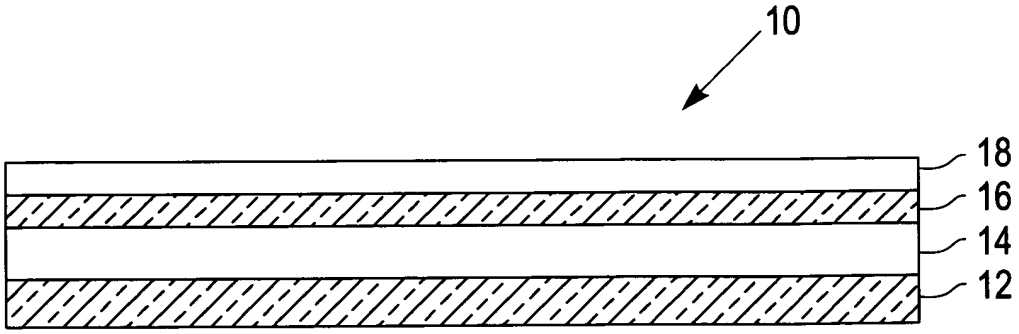

Fig. 2a

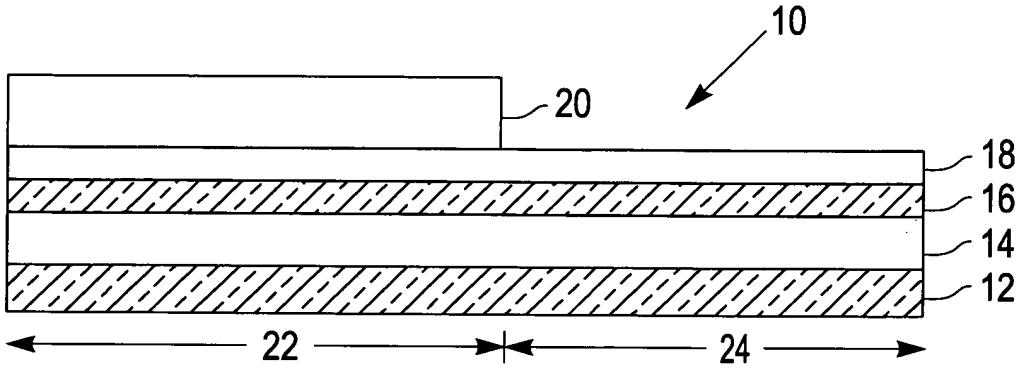

Fig. 2b

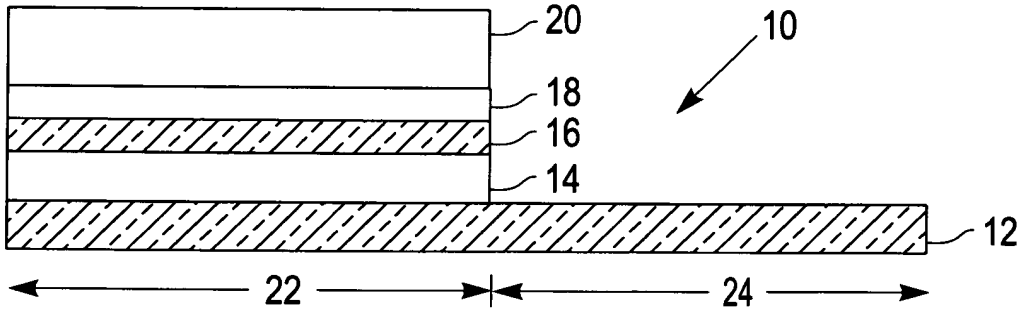

Fig. 2c

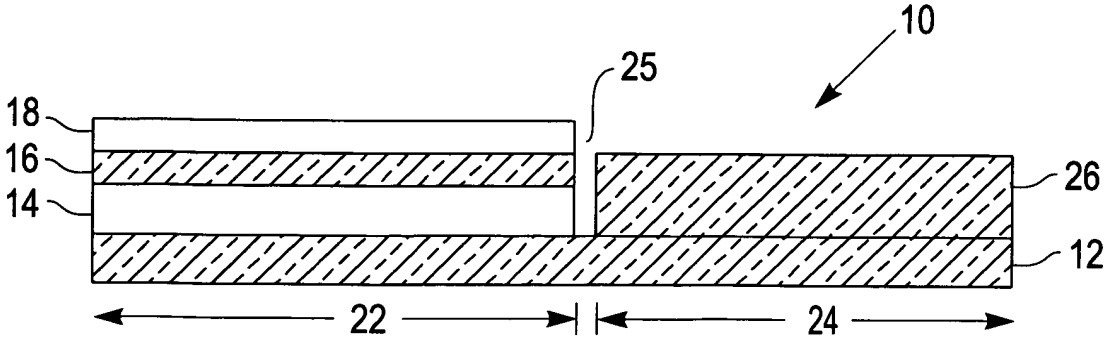

Fig. 2d

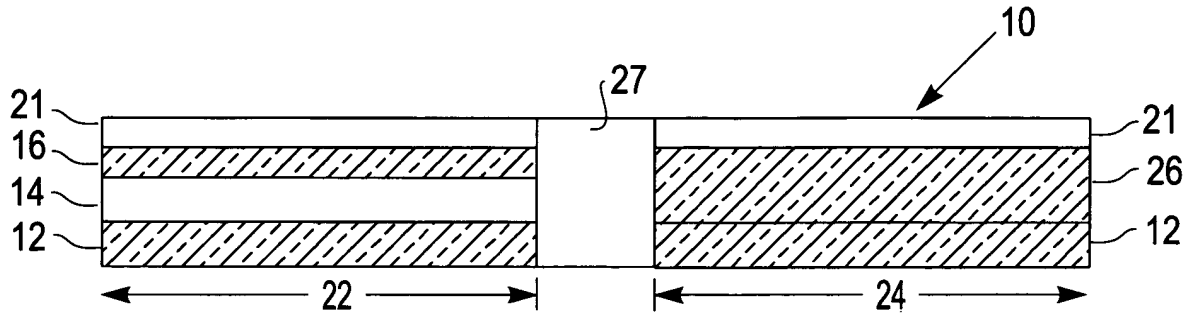

Fig. 2e

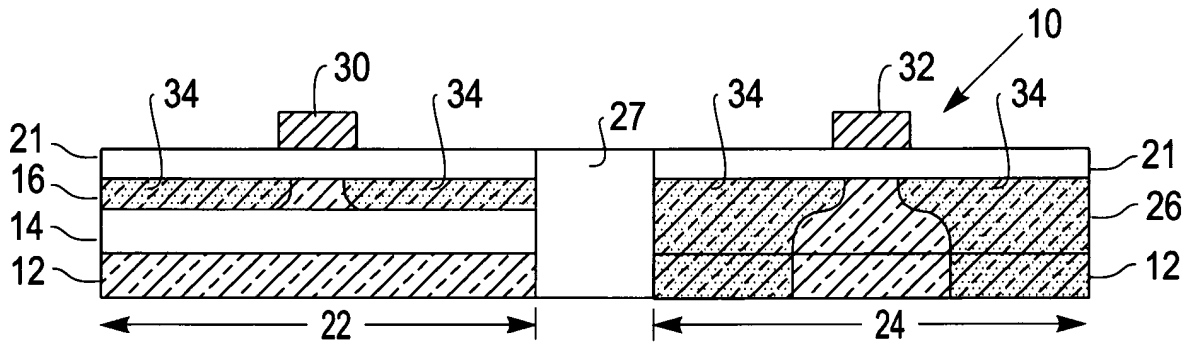

Fig. 2f

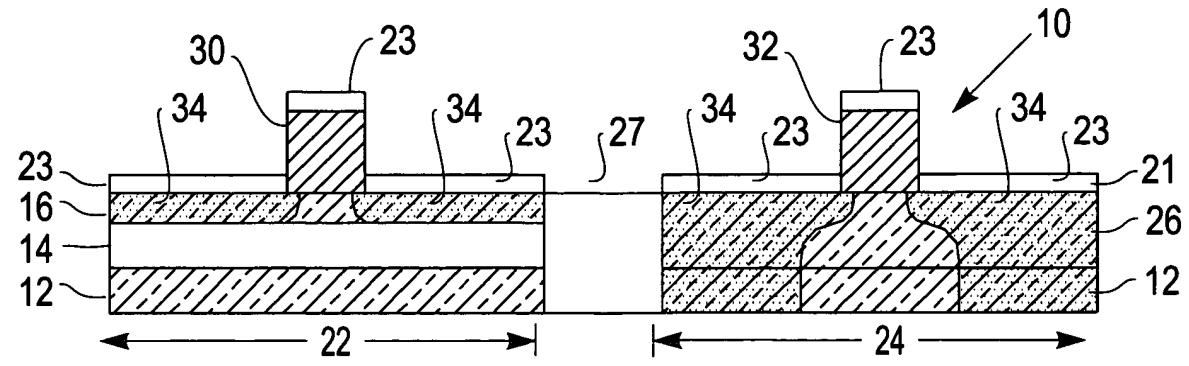

Fig. 2g

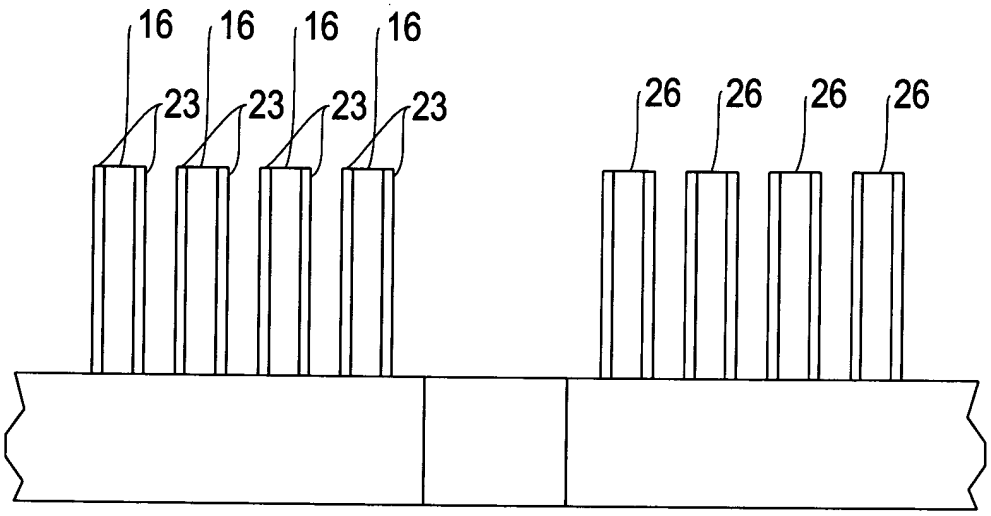
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Fig. 2h

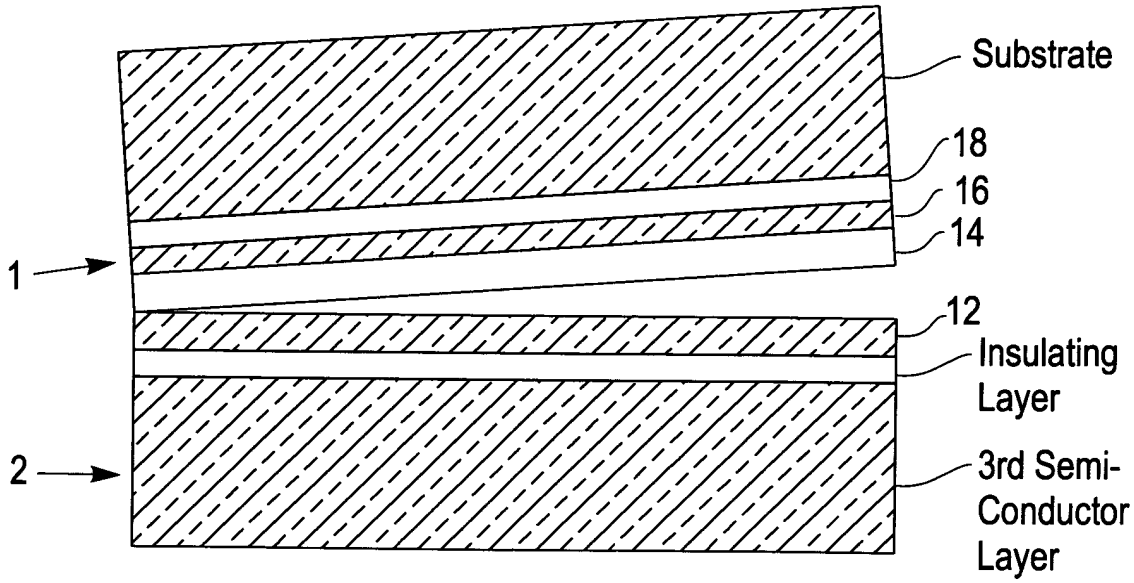

Substrate
18
16
14
1
12
Insulating Layer
2
3rd Semi-Conductor Layer

Fig. 3a

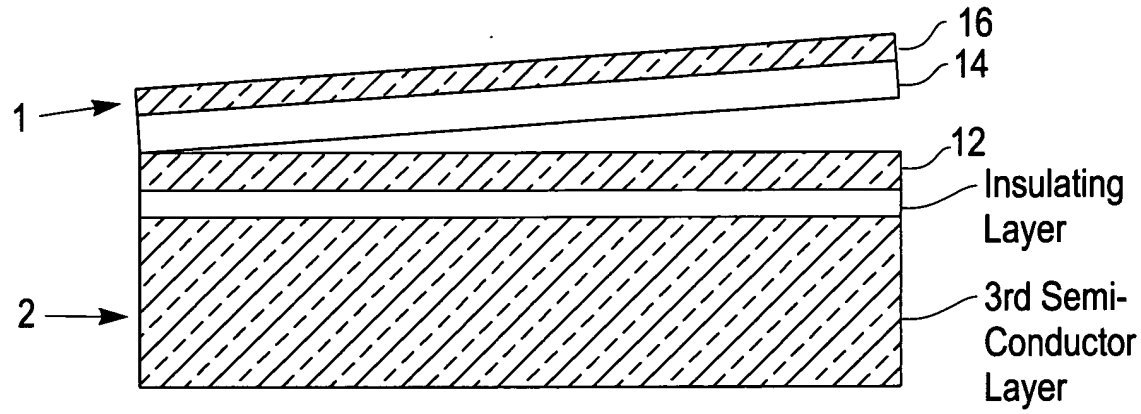
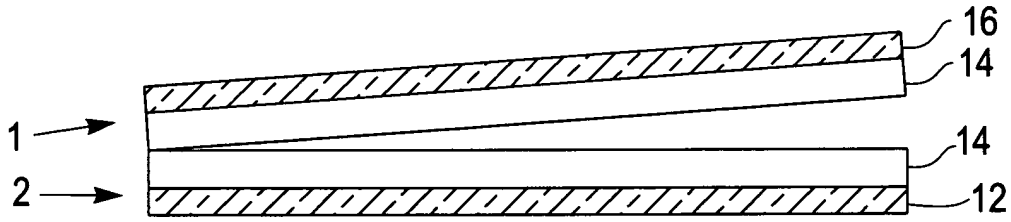


Fig. 3b



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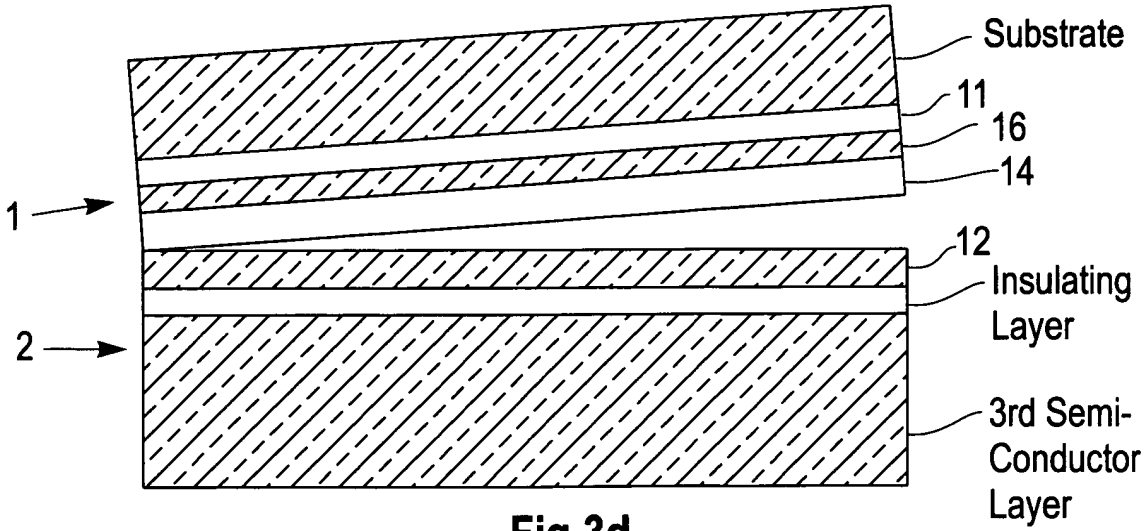


Fig.3d

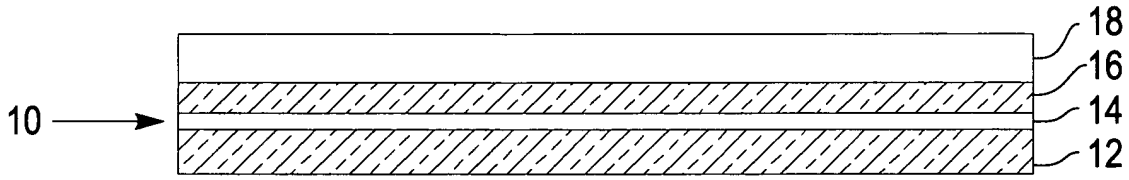


Fig.4a

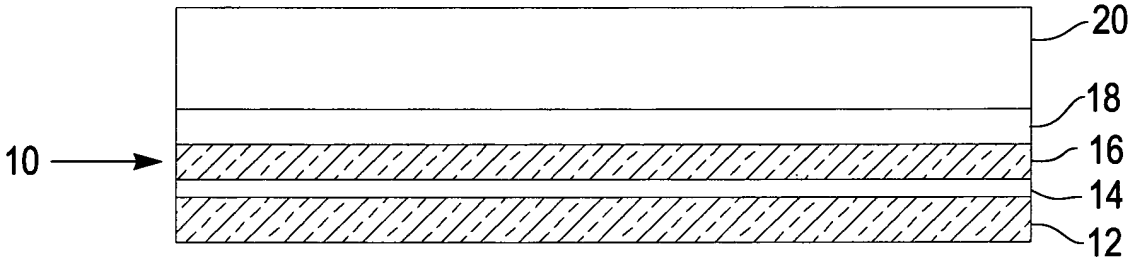


Fig.4b

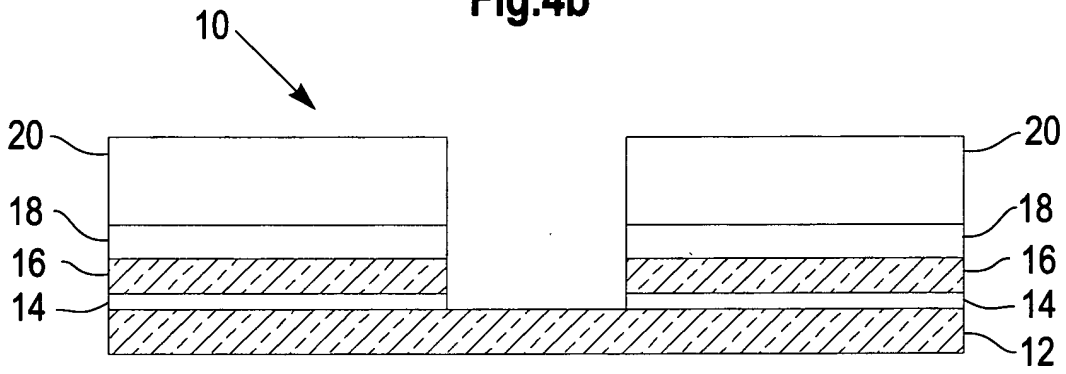
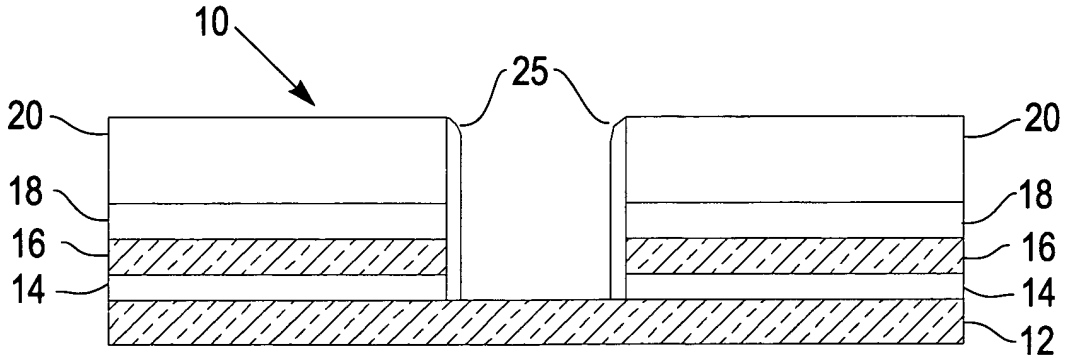


Fig.4c



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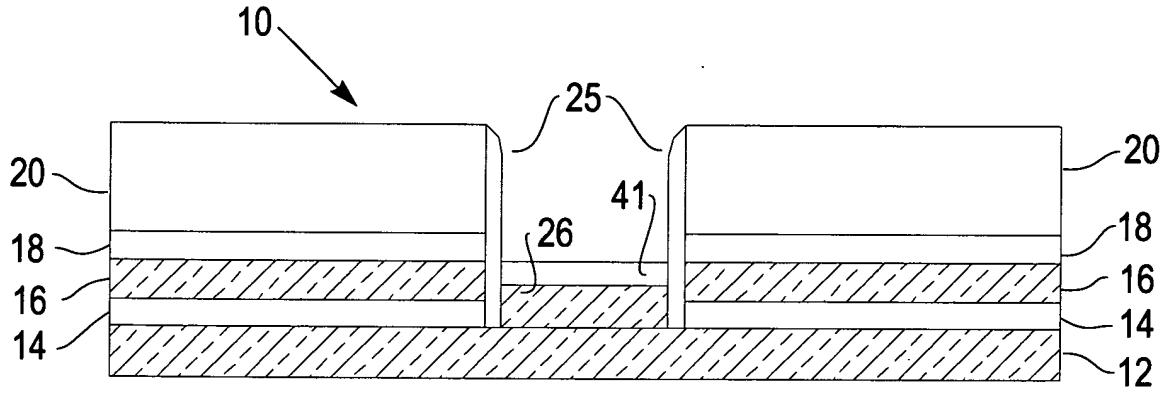


Fig.4e

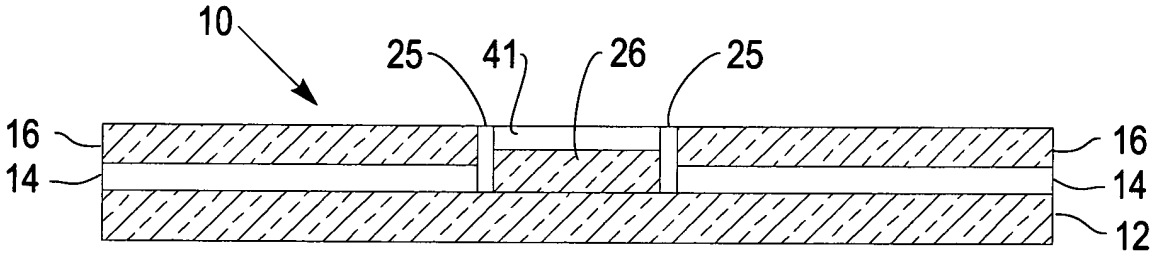
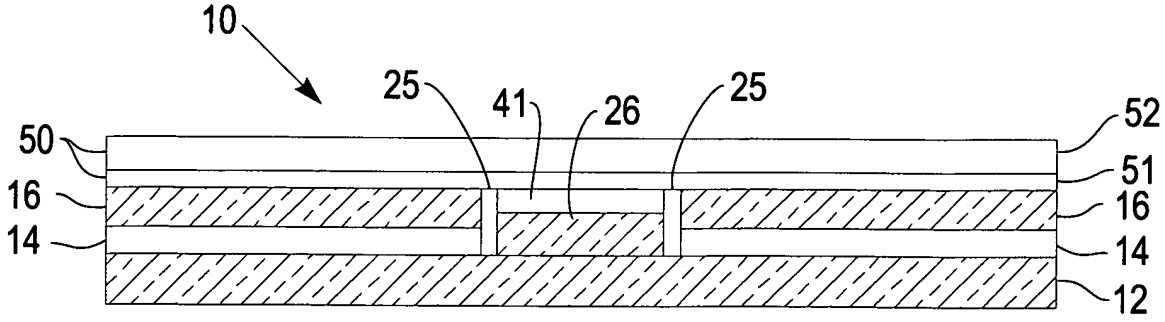


Fig.4f



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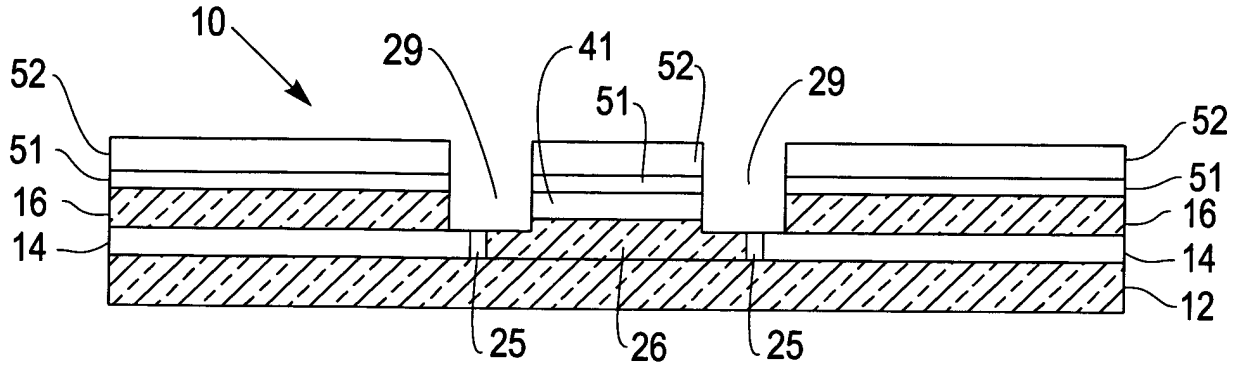

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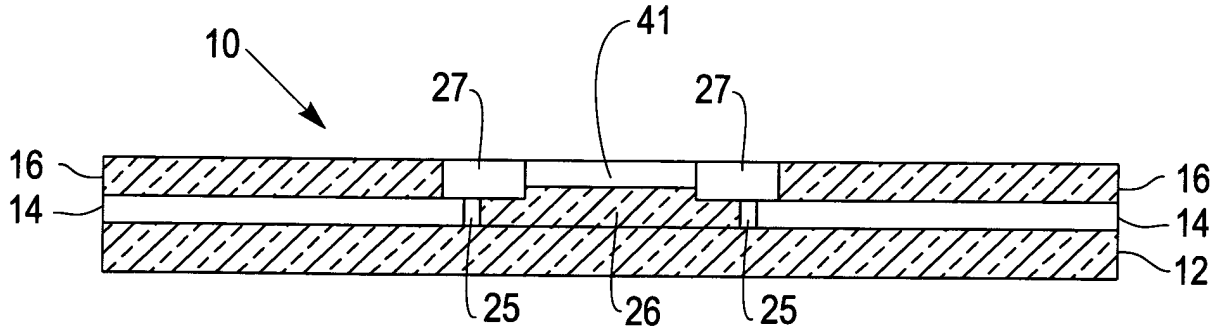


Fig.4i

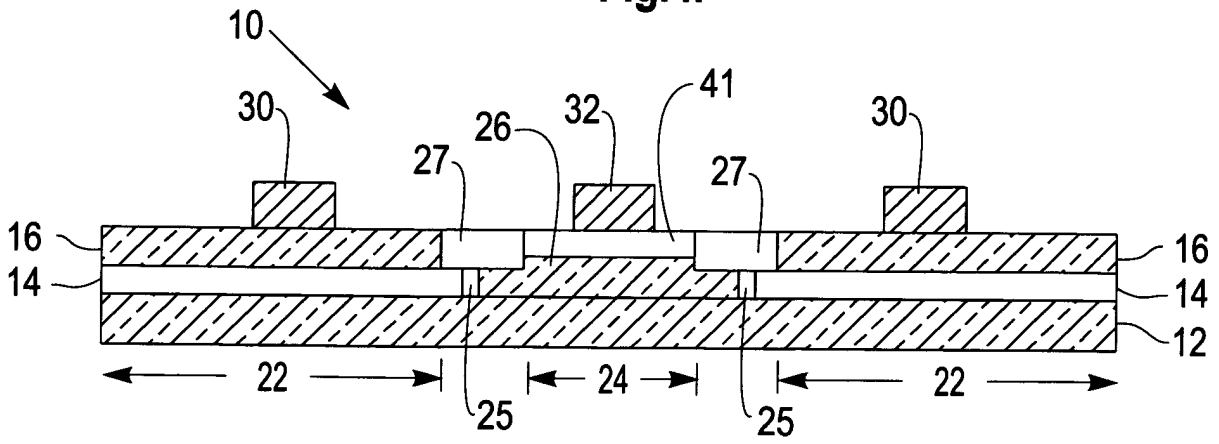


Fig.4j

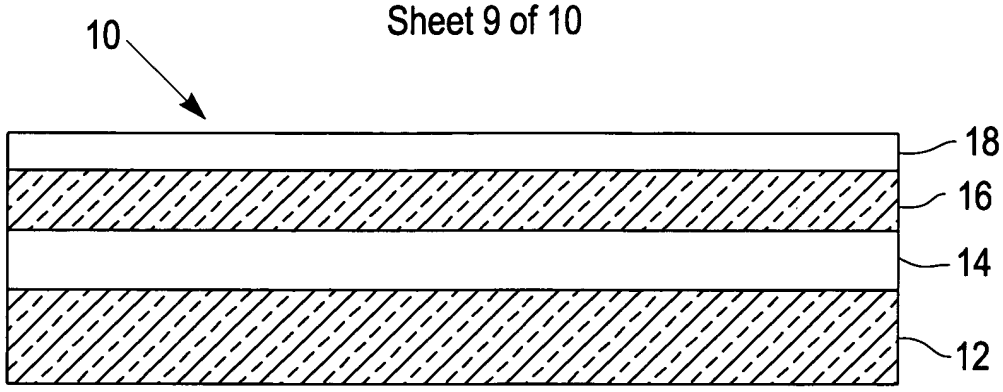


Fig.5a

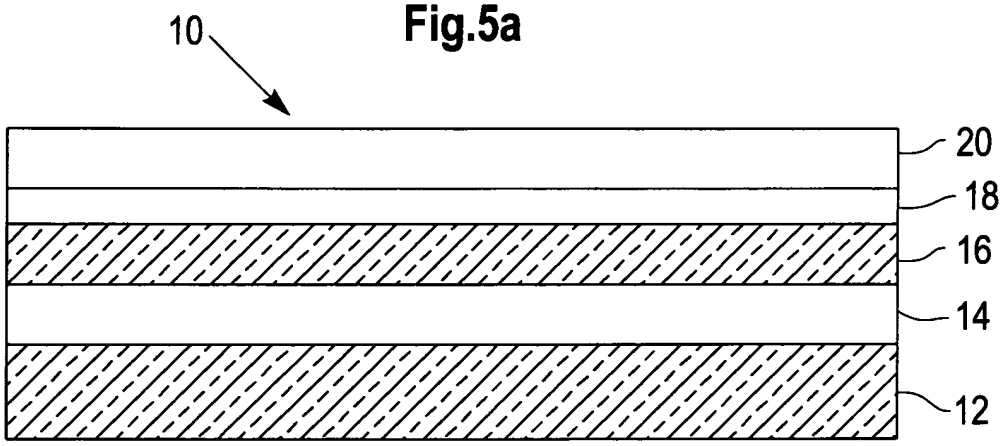
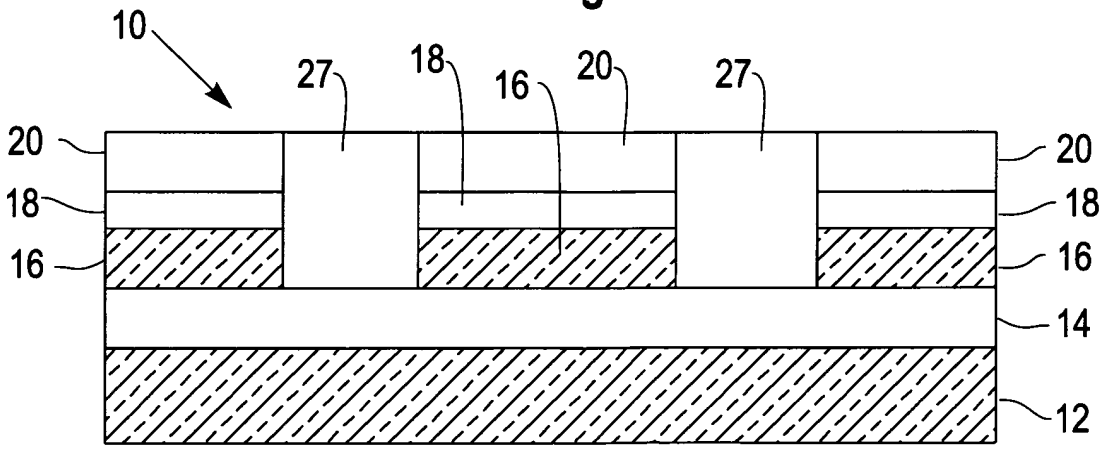


Fig.5b



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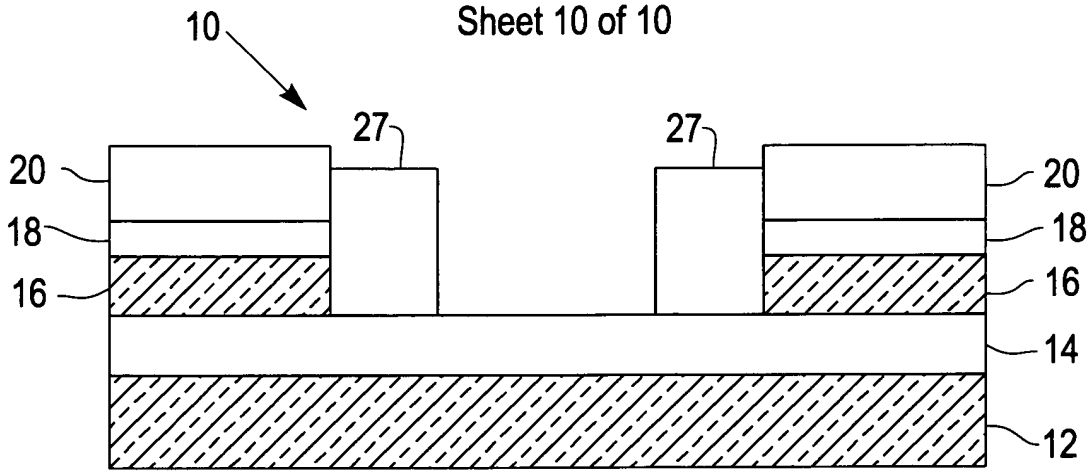

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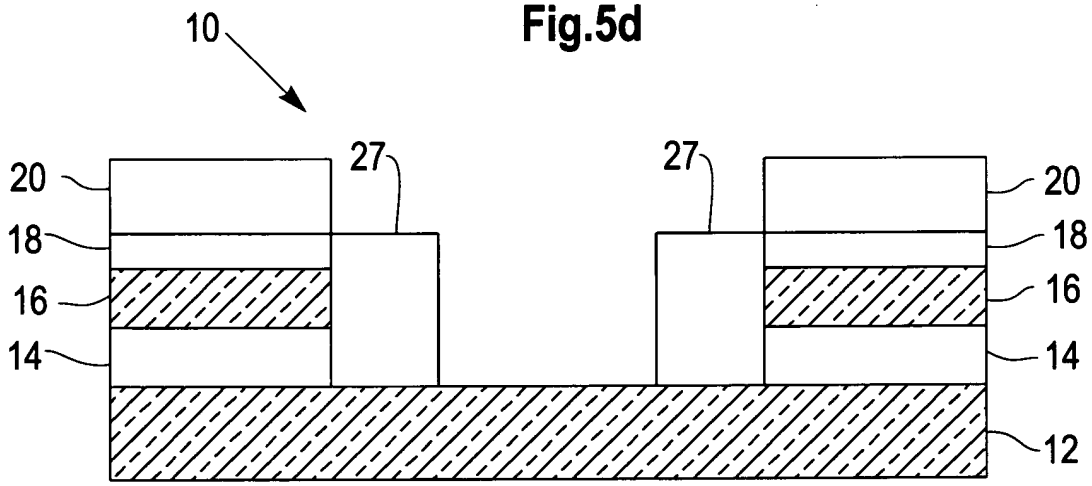

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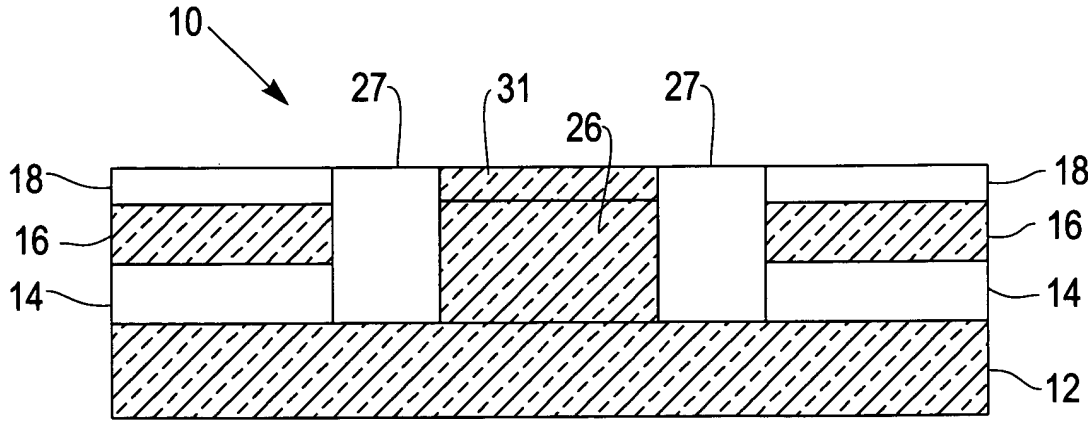

Fig.5f